IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No: Unassigned (Parent App. No. 09/823,151) Docket No:	assigned
Filed: February 26, 2004) Date: February 26	Unassigned
CHEMICAL MECHANICAL POLISHING PRESSURE) TO FORCE TO BE APPLIED BY POLISHING HEAD TO) WAFER)	LAM2P255D.

CERTIFICATE OF EXPRESS MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service on Exbrusury 26, 2004 in an envelope as "Express Mail Post Office to Addressee" service under 37 CFR §1, 10, Mailing tabel Nymber EV381562404US, addressed to the Commissioner for Payerts, Attendation, 22213-1450. Signed:

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR \$\$1.56 AND 1.97

Mail Stop: DD Commissioner for Patents Alexandria, VA 22313-1450

Dear Sir:

Enclosed is a PTO Form 1449 listing references to be made of record. Copies of each of the identified references have been previously submitted in the parent case having Serial Number <u>09/82.151</u>, filed on March 29, 2001, from which priority is claimed under 35 U.S.C. § 120, and may be material to the examination of the above-identified patent application. Applicant submits this list of references in compliance with their duty of disclosure pursuant to 37 CFR §§1.56 and 1.97. The Examiner is requested to make these cliations of official record in this application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

It is respectfully submitted that no fee is due in conjunction with the filing of this IDS because the Information Disclosure Statement is being filed before the first Office Action. However, should the Commissioner determine that additional fees are required for filing the present Information Disclosure Statement, the Commissioner is hereby authorized to charge any such fees to our deposit account number 50-0805 (Order No: LAM2P2S5D2). A copy of this sheet is enclosed.

Respectfully submitted, MARTINE & PENILLA, LLI

Chester E. Martine, Jr. Reg. No. 19,711

710 Lakeway Drive, Suite 170 Sunnyvale, CA 94085 Telephone: (408) 749-6900 Customer Number 25920

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

M. Saldana

Application No: Unassigned (Parent App. No. 09/823,151)

Filed: February 26, 2004

For: APPARATUS WITH RESOLUTION
ENHANCEMENT FEATURE FOR IMPROVING
ACCURACY OF CONVERSION OF REQUIRED
CHEMICAL MECHANICAL POLISHING PRESSURE
TO FORCE TO BE APPLIED BY POLISHING HEAD TO

WAFER

Group: Unassigned

Examiner: Unassigned

Docket No: LAM2P255D2

Date: February 26, 2004

CERTIFICATE OF EXPRESS MAILING

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INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR §§1.56 AND 1.97

Mail Stop: DD Commissioner for Patents Alexandria, VA 22313-1450

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Respectfully submitted, MARTINE & PENILLA.

Chester E. Martine, Jr.

Reg. No. 19,711

710 Lakeway Drive, Suite 170 Sunnyvale, CA 94085 Telephone: (408) 749-6900 Customer Number 25920

Form 1449 (Modified)	Attorney Docket No:	U.S.
, ,	LAM2P255D2	Unassigned
Information Disclosure	Applicant:	_
Statement By Applicant	M. Saldana	
1 '''	Filing Date:	Group:
(Use Several Sheets if Necessary)	February 26, 2004	Unassigned

U.S. Patent Documents

Examiner						Sub-
Initial	No.	Patent No.	Date	Patentee	Class	class
/CK/	Α	5,623,318	04/1997	Lee	348	614
/CK/	В	5,916,012	06/1999	Pant et al.	451	41
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	G					
	H					
	J			•		
	K					

Foreign Patent or Published Foreign Patent Application

Examiner		Document	Date	Country or		Sub-	Translat	ion
Initial	No.	No.		Patent	Class	class	Yes	No
				Office		İ		
/CK/	L	GB 2 162 977 A	12/2/86	EPO	H03M	7/00		X
	М							
	N							

Other Documents

Examiner					
Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication			
/CK/	0	F.S. Lai, "The Architecture and Analysis of a Hybrid Number System Processor",			
		1992 IEEE, IBM Thomas J. Watson Research Ctr., Yorktown Heights, NY.			
	Р	Samueli, H., "An Improved Search Algorithm for the Design of			
/CK/		Multiplierless FIR Filters with Powers-of-Two Coefficients", pp. 1044-			
7010		1047, IEEE Transactions on Circuits and Systems, Vol. 36, No. 7, July 1047, IEEE Transactions on Circuits and Systems, Vol. 36, No. 7, July 1047, IEEE Transactions on Circuits and Systems, Vol. 36, No. 7, July 1047, IEEE Transactions on Circuits and Systems, Vol. 36, No. 7, July 1047, IEEE Transactions on Circuits and Systems, Vol. 36, No. 7, July 1047, IEEE Transactions on Circuits and Systems, Vol. 36, No. 7, July 1047, IEEE Transactions on Circuits and Systems, Vol. 36, No. 7, July 1047, IEEE Transactions on Circuits and Systems, Vol. 36, No. 7, July 1047, IEEE Transactions on Circuits and Systems, Vol. 36, No. 7, July 1047, IEEE Transactions on Circuits and Systems, Vol. 36, No. 7, July 1047, IEEE Transactions on Circuits and Systems, Vol. 36, No. 7, July 1047, IEEE Transactions on Circuits and Systems of Circuits and Cir			
		1989.			
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/CK/		Arithmetic", pp. 389-400, IRE Transactions on Electronic Computers			
	ł	Sept. 1961.			
Examiner	-		Date Considered		
	/Cha	rles Kasenge/	04/24/2008		

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.